

SCAN TEST APPARATUS FOR TESTING OF
PRINTED CIRCUIT BOARDS

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ABSTRACT OF THE DISCLOSURE

A scan test apparatus having at least an upper layer of conductive and compliant material and may include a lower layer of conductive and compliant material sized to cover the upper and lower surfaces of the printed circuit board to be tested. Electrical current is introduced into the conductive layers which shorts out the circuits on the printed circuit board. An electrical contactor is positioned on either side of the conductive layers on both sides of the printed circuit board. The printed circuit board is passed through the upper and lower conductive layers and the contactors by rollers positioned on each end of the scan test machine. The contactor sends a test signal from the circuit board to measurement electronics. Other embodiments include the shorting matrix to be movable and the printed circuit board being fixed and include non-contact sensors or arrays of electrical contactors.

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